```
39  /* Block states (default storage) for system '<Root>' */
40  typedef struct {
41    real_T DiscreteTimeIntegratorz2_DSTATE;
42                                       /* '<S1>/Discrete-Time Integrator (z2)' */
43    real_T UD_DSTATE;                   /* '<S2>/UD' */
44    real_T DiscreteFilterz2_states;     /* '<S1>/Discrete Filter (z2)' */
45    real_T DiscreteTimeIntegratore_x_DSTAT;
46                                      /* '<S1>/Discrete-Time Integrator (e_x)' */
47    real_T DiscreteTimeIntegratore_y_DSTAT;
48                                      /* '<S1>/Discrete-Time Integrator (e_y)' */
49    real_T UnitDelayStateestimator_DSTATE[2];/* '<S1>/Unit Delay (State estimator)' */
50  } DW_Inner_T;
51
52  /* Constant parameters (default storage) */
53  typedef struct {
54    /* Expression: ct2.Cod
55     * Referenced by: '<S1>/Cod'
56     */
57    real_T Cod_Gain[8];
58
59    /* Expression: ct2.Fa
60     * Referenced by: '<S1>/Fa'
61     */
62    real_T Fa_Gain[8];
63
64    /* Expression: ct2.Bod
65     * Referenced by: '<S1>/Bod'
66     */
67    real_T Bod_Gain[8];
68
69    /* Expression: ct2.Aod
70     * Referenced by: '<S1>/Aod'
71     */
72    real_T Aod_Gain[4];
73  } ConstP_Inner_T;
74
75  /* External inputs (root inport signals with default storage) */
76  typedef struct {
77    real_T gx;                          /* '<Root>/gx' */
78    real_T gy;                          /* '<Root>/gy' */
79    real_T gz;                          /* '<Root>/gz' */
80    real_T x_s1;                        /* '<Root>/x_s1' */
81    real_T y_s1;                        /* '<Root>/y_s1' */
82    real_T z_s1;                        /* '<Root>/z_s1' */
83    real_T motor_thr;                   /* '<Root>/motor_thr' */
```

```
84   } ExtU_Inner_T;
85
86   /* External outputs (root outports fed by signals with default storage) */
87   typedef struct {
88     real_T motor1_pwm;                  /* '<Root>/motor1_pwm' */
89     real_T motor2_pwm;                  /* '<Root>/motor2_pwm' */
90     real_T motor3_pwm;                  /* '<Root>/motor3_pwm' */
91     real_T motor4_pwm;                  /* '<Root>/motor4_pwm' */
92     real_T x_s2;                        /* '<Root>/x_s2' */
93     real_T y_s2;                        /* '<Root>/y_s2' */
94     real_T z_s2;                        /* '<Root>/z_s2' */
95   } ExtY_Inner_T;
```