

図A ビルドの様子

```
$ cargo espflash --release --monitor
Serial port: /dev/ttyACM1
Connecting...

    Compiling proc-macro2 v1.0.43
    Compiling quote v1.0.21
    Compiling unicode-ident v1.0.3
...
    Finished release [optimized] target(s) in 1m 31s
```

図B フラッシュ・メモリへの書き込み

```
Chip type:          ESP32-C3 (revision 3)
Crystal frequency: 40MHz
Flash size:         4MB
Features:           WiFi
MAC address:        84:f7:03:27:1b:c0
App/part. size:     356624/1048576 bytes, 34.01%
[00:00:00] ##### 12/12      segment 0x0
[00:00:00] ##### 1/1        segment 0x8000
[00:00:03] ##### 191/191    segment 0x10000
Flashing has completed!
```

図C ESP32-C3がリセットされて書き込んだプログラムが起動する

```
Commands:
  CTRL+R  Reset chip
  CTRL+C  Exit

ESP-ROM:esp32c3-ap1-20210207
Build:Feb 7 2021
# ...中略...
I (284) sleep: Configure to isolate all GPIO pins in sleep state
I (290) sleep: Enable automatic switching of GPIO sleep configuration
I (297) cpu_start: Starting scheduler.
```