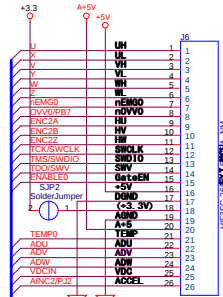


U2 TPM4MNFYAFG



Diagram of a 3-bit shift register. The inputs are 1, 0, 0. The outputs are 1, 0, 0.



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